## **NALLA NARASIMHA REDDY**

Education Society's Group of Institutions - Integrated Campus Approved by AICTE, New Delhi, Affiliated to JNTU - Hyderabad

## SCHOOL OF ENGINEERING

## DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Name of the Subject: Analog and Digital Electronics Subject Code: CS301ES

Year/Sem : II-I Regulation : R18

CO1	Differentiate various components and devices with characteristics.
CO2	Analyze and design various transistor amplifiers using BJTs and FETs.
CO3	Utilize the postulates of the Boolean Algebra to minimize the Combinational circuits.
CO4	Design and Analyze Combinational and Sequential circuits.
CO5	Design the logic gates using different Logic families.

Mapping Matrix of CO's and PO's:

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10	P11	P12	PSO1	PSO2
CO1	X	X	X											
CO2	X		X	X	X					X		X		
CO3	X	X	X	X	X									
CO4	X	X	X	X	X					X				
CO5	X	X	X	X	X							X		